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**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Koji YAMAGUCHI

Group Art Unit: 2822

Application No.: 10/753,357

Examiner: R. Potter

Filed: January 9, 2004

Docket No.: 118292

For: WIRING SUBSTRATE, SEMICONDUCTOR DEVICE, SEMICONDUCTOR  
MODULE, ELECTRONIC EQUIPMENT, METHOD FOR DESIGNING WIRING  
SUBSTRATE, METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE,  
AND METHOD FOR MANUFACTURING SEMICONDUCTOR MODULE

**AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In reply to the September 28, 2005 Office Action, please consider the following:

**Amendments to the Claims** as reflected in the listing of claims; and

**Remarks.**